



ASIC front-end for Position Sensitive SiPM and Vacuum PMT with gain adjustment and depth of interaction measurement

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### **Research Project Collaboration**

### ■ IFIC (Corpuscular Physics Research Institute)

- □ High Energy Physics research.
- Nuclear Medical Instrumentation
  - Portable Gamma Camera for medical applications
  - Development of PET systems.
    - PET mammography.
    - Small Animal PET.



### **DSD** (Digital Systems Design)

- Mixed Signal, front-end and read-out electronics for high energy phisics.
- High-Speed Data Acquisition and Digital Signal
   Processing Systems for PET Mammography.
- □ Mixed Signal ASIC development.







### **Detector Front-End**

First Development of Detector Front-End and Read-Out



[1] S. Siegel, R. W. Silverman, Y. Shao, and S. R. Cherry, "Simple charge division readouts for imaging scintillator arrays using a multi-channel pmt," *IEEE Trans. Nucl. Sci.*, vol. 3, no. 43, pp. 1634–1641.

**[2]** J. D. Martinez, J. M. Benlloch, J. Cerdà, Ch. W. Lerche, N. Pavon, A. Sebastia. "High-speed data acquisition and digital signal Processing system for PET imaging techniques applied to mammography" *IEEE Trans. Nucl. Sci.*, Volume 51, Issue 3, Part 1, June 2004 Page(s):407 - 412





### **Detector Front-End**

New system requirements and extended features.

- □ Depth of Interaction Measurement.
- □ Individual Anode Gain Adjustment.
- Possible use of different types of Photomultipliers (SiPMs).
- □ Reduce Front-End delay and Increase timing and spatial resolution.





**Mixed Signal** 

**ASIC** 



### Anode Preamplifier (I)





## Anode Preamplifier (II)

- Low Input Impedance increases Bandwidth
- Gain Adjustment compensates Anode Gain dispersion.
- Isolates PM from Resistor Network.
  - Resistor values can be designed to optimize V<sub>Ch</sub> (increases DOI precision)
  - Reduction of capacitances in Resistor Network input nodes



\* Including Cout PMT ≈ 10pF



\*\* AMS 0.35um - Extracted layout





## Anode Preamplifier (III)

- Conection to PMT and SiPM
  - PMT Scheme







# Anode Preamplifier (IV)

□ SiPM Scheme. Biasing.



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 Centre of Gravity of total energy of gamma event:

$$X = \frac{(I_a + I_c) - (I_b + I_d)}{I_a + I_c + I_b + I_d} \quad Y = \frac{(I_a + I_b) - (I_c + I_d)}{I_a + I_b + I_c + I_d}$$

Sources of error in Impact Position Calculation







Error in Impact Position Calculation

- □ **PreAmplifier Noise:** Very Low (~40nA) and affects uniformly all network inputs.
- Thermal Noise from Resistor Network:
- Instantaneous noise generated by  $\text{in}^2{}_{\rm V}$  in Ia and Ic is anticorrelated (same as in Ib and Id).
- Noise from upper and lower horizontal resistor chain is uncorrelated.

$$\frac{\overline{(I_a + I_c)_n^2}}{(I_b + I_d)_n^2} = 2.\overline{i_{nH}^2} \\
\frac{\overline{(I_b + I_d)_n^2}}{(I_a + I_c)_n^2} = 2.\overline{i_{nH}^2} \\
X = \frac{(I_a + I_c) - (I_b + I_d)}{I_a + I_c + I_b + I_d} \\
= \frac{(I_a + I_c) - (I_b + I_d)}{(I_a + I_c)_n^2} \\
= \frac{(I_a + I_c) - (I_b + I_d)}{(I_a + I_c + I_b + I_d)} \\
= \frac{\overline{(X^2 - \frac{\partial X}{\partial (I_b + I_d)}}}{(I_a + I_b + I_c + I_d)^2} \\
= \frac{\partial X}{\partial (I_a + I_c)} \\
= \frac{\partial X}{\partial (I_a + I_c)_n^2} \\
= \frac{\partial X}{\partial (I_a + I_c$$







- Error in Impact Position Calculation
  - Thermal Noise from Resistor Network. Conclusions

$$\overline{\Delta \mathbf{Y}^2} = \frac{\mathbf{n}.4\overline{\mathbf{i}_{nV}^2}}{\left(\mathbf{I}_a + \mathbf{I}_b + \mathbf{I}_c + \mathbf{I}_d\right)^2} \quad \overline{\Delta \mathbf{X}^2} = \frac{8.\overline{\mathbf{i}_{nH}^2}}{\left(\mathbf{I}_a + \mathbf{I}_b + \mathbf{I}_c + \mathbf{I}_d\right)^2}$$

1. 
$$\Delta X \neq \Delta Y$$

Do not depend on impact position



 $(V_{chMAX} < Vdd) \rightarrow (la+lb+lc+ld)$  must decrease

$$\left(\boldsymbol{I}_{a}+\boldsymbol{I}_{b}+\boldsymbol{I}_{c}+\boldsymbol{I}_{d}\right)=\frac{V_{chMAX}}{\mathbf{K}.\boldsymbol{R}}$$

K factor depends on impact depth <sup>[3]</sup>

[3] C. W. Lerche, J. M. Benlloch, F. Sanchez, N. Pavon, B. Escat, E. N. Gimenez, M. Fernandez, I. Torres, M. Gimenez, A. Sebastia, and J. Martinez, "Depth of gamma-ray interaction within continuous crystals from the width of its scintillation light-distribution" *IEEE Trans. Nucl. Sci.*, vol. 52, no. 3, pp. 560–572, 2005.









Error in Impact Position Calculation

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- □ Frequency Response of Resistor Network <sup>[4]</sup>
  - Current Signal Amplitudes are affected by Frequency Response of resistor network (Capacitances in input nodes).
  - Frequency response depends on input node position.
  - Horizontal chain resistors 10 times lower → Horizontal resistor chains effects not included.
  - Currents in different input points will not sum in phase // Higher amplitude signals are close to centre of impact.



[4] A. Pullia, W. F. J. Muller, C. Boiano, and R. Bassini, "**Resistive or capacitive chargedivision readout for position-sensitive detectors**" *IEEETrans. Nucl. Sci.*, vol. 49, no. 6, pp. 3269–3277, 2002.



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### **Impact Position Calculation**

□ Frequency Response of R. Network Read-Out.





### Error in Impact Position Calculation

- □ Evaluation of error due to Frequency response of R. Network Read-Out.
  - Gaussian Pulse (10n rise / 50n decay, LSO-PMT systems).
  - Measure the position error in every input point of a vertical resistor chain.





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### Depth of Interaction Measurement

- □ Depth of Gamma-Ray Interaction within a continuous scintillator crystal is related to the width of the generated light-distribution <sup>[3]</sup>.
  - The light-distribution width can be calculated with the sum of all anode voltages.



[3] C. W. Lerche, J. M. Benlloch, F. Sanchez, N. Pavon, B. Escat, E. N. Gimenez, M. Fernandez, I. Torres, M. Gimenez, A. Sebastia, and J. Martinez, "Depth of gamma-ray interaction within continuous crystals from the width of its scintillation light-distribution" *IEEE Trans. Nucl. Sci.*, vol. 52, no. 3, pp. 560–572, 2005.





### Conclusions

- PreAmplifier stage isolates read-out from PM.
- Gain Adjustment can equalize dispersion of PM characteristics.
- Frequency Response improvement in ASIC implementation reduces overall front-end delay.
- Implementation of ASIC front-end increases timing and spatial resolution.

