Processing and measurements of GaAs pixel detectors



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Outline

- Processing of pixel detectors
- GaAs for detector applications (revival)
- Measurements with GaAs Medipix assemblies
- Comparison with Silicon assembly
- Summary and outlook







Medipix Activities Freiburg



Processing of pixel detectors (FMF)

- pixels sizes down to 55 µm (pixel Medipix2)
- 4000 65.000 pixels
- Low temperature processing (< 200 °C)
- Polymere passivation (BCB)
- Low force Flip-Chip-Bonding
- Processing of single detectors or wafers









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Detector assembly after flip-chip process







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CdTe – pixel detector Medipix1







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Medipix 2: Processing features

Pixel size on the detector side can be increased $(110x110\mu m^2, 165x165\mu m^2, ...)$ bonding only 1 out of 4 / 9 /... pixels on the MP2 chip

55x55µm² every MP2 pixel used



110x110µm² every 4th MP2 pixel used







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GaAs pixel detectors (LEC material from commercial supplier)



Properties of GaAs radiation detectors

- Semi-insulating material
- Available technology
- Good absorption (< 50 keV)
- Wafer sizes available up to 6 inch
- 55 µm resolution possible (small effect of fluorescence)

Strong development over the last 5 years:

• Reduction of defect concentration (EL2)

Performance is depending on bulk properties







GaAs 1x1 assembly processed and bonded at FMF





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Detector bias at 270V I = 50μ A

Am-source





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Detector bias at 275V I = 400µA

Am-source



Detector bias at 275V I = 50μ A

x-ray tube



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Detector bias at 280V I = 300µA

x-ray tube







Freiburger Materialforschungszentrum High leakage current

Cell structure at low bias







GaAs 55x55µm² detector



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GaAs 110x110µm² detector



Flatfield acquisition (W 60kV, 1.5mm Al)

without correction, only th.adj.mask is activated



270V





Flatfield acquisition (W 60kV, 1.5mm Al)

without correction, only th.adj.mask is activated



190V



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X-ray images of TTL-chips



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MTF comparison GaAs



FMF

IWoRID 2006 Pisa





Energy calibration for 300µm GaAs assembly



Comparison of integrated counts



same acqu.time source: 241 Am 59.5keV Tb K_a 44.2keV

--: 300µm GaAs

-- : 700µm Si



Advantages of "pixel binning":

SNR will be increased

 if unused pixels are switched off, THL can be set to lower values

 pitch can be adapted to application + material and/or thickness

- charge sharing can be reduced

Disadvantages:

 one pixel has to compensate 4/9 times higher leakage current

lower spatial resolution



- counters are "full" very quickly \rightarrow more frequent readout

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Conclusion

- Flip-chip-process successful for GaAs
- 55µm and 110 µm pixel detectors
- MTF and absorption (for higher E) are at the theoretical limit
- material not fully depleted
 - processing of 250 µm wafers
 - higher bias (HV passivation)

Research on material properties:

- Comparison of LEC and VGF material
- Processing of QUAD





Thanks for your attention!





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