

University of Wente

An integrated single photon detector array using porous anodic alumina



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Overview

• Goal:

A prototype single photon detector array manufactured monolithically on an imaging ASIC like Medipix2

- Idea and motivation
- Requirements of the MCP
- First experimental results
- Conclusions and outlook

The idea

Detector system:

- Photo-Cathode
- Electron multiplication structure
- Medipix imaging ASIC



Existing Knowledge

Belarus academy of science + Samsung Research: Alumina MCPs





Anodic alumina made in MESA+ (Univ. Twente)



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The porous alumina MCP detector



Using micro-electronics technology:

- other geometries
- structural integrity (substrate)
- different materials
- different voltages ?

Geometry Requirements I

Gain



- Saturation at gain-diameter ratio of around 10¹¹ -/m
- Lower limit: 10 nm → No Nano-Channel Plate
- MCP with G > 1000 \rightarrow d > 0.1 µm and α > 50 \rightarrow L > 5 µm

Standard models by Wiza (NIM '79) and Eberhardt (Appl Opt. '79)

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Geometry Requirements II

Varying aspect ratio (α)

Dielectric strength limits voltage





Standard models by Wiza (NIM '79) and Eberhardt (Appl Opt. '79)

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Discrete porous alumina disks

 Available from Whatman for fluid filtration, 60 µm thick, 0.2 µm pores





Metallization of alumina disk



- 10 nm Au/Pd
- Pores still open
- Au penetration around 0.2 µm

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Measurements on alumina disks

- Metal contacts defined
- Capacitance-Voltage measurements prove functionality
- Investigation of bias capability
- Functional measurement on dimpled disks





Bias curve for alumina disks



- Some samples are clearly more leaky than others
- Current densities not excessive
- Area of Medipix2 pixel (55×55 μ m²) \rightarrow current < 0.6 nA

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Porous Al₂O₃ made by anodization



~ 100 - 150 V → 200 nm

Integration challenge



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Conclusions

- Integrated MCP based on porous alumina
- Promising experiments on discrete disks
- Functional metallization
- Reverse bias > 1500 V, limited leakage
- First anodization experiment successful
- Integration scheme for wafer-scale post-processing





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